

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13	(Mambakkam-Sreenath.in. Venkidu-Arockiyaswamy.in. Jones-Larry-Lawson.in.) and @pd>"20040202"	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2005/07/19 16:14
L2	1	("6650980").PN.	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:14
L4	357	(700/231.ccls. 361/737.ccls. 710/302.ccls. 710/301.ccls. 710/64.ccls. 710/63.ccls. 710/62.ccls. 235/492.ccls. 235/435.ccls. 235/439.ccls. 235/440.ccls. 235/441.ccls.) and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:18
L5	7	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 17:01
L6	1	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) and @pd>"20050202"	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 16:53
L7	0	(scsi (small adj computer adj system adj interface)) near5 (simple adj display adj device) and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:53
L8	19	(scsi (small adj computer adj system adj interface)) near5 (display screen lcd) and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:53
L9	1	(scsi (small adj computer adj system adj interface)) near5 (display screen lcd) near5 (command\$3 instruction\$1) and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:56

L10	0	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1)) same parallel and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:56
L11	0	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3) and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:57
L12	3	((memory smart ic chip circuit) adj1 card\$1) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3) and @pd>"20050202"	US-PGPUB ; USPAT	OR	OFF	2005/07/19 16:57
L13	0	((memory smart ic chip circuit) adj1 card\$1) near5 (plural\$3 many several multiple) near5 (parallel simultaneous\$2 concurrent\$2) near5 (access\$3 read\$3 writ\$3 communicat\$3) and @pd>"20050202"	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 17:00
L14	1	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) near5 (copy\$3 transfer\$4)	US-PGPUB ; USPAT	OR	OFF	2005/07/19 17:15
L15	67	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple first second) near5 (copy\$3 transfer\$4)	US-PGPUB ; USPAT	OR	OFF	2005/07/19 17:16

L16	0	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple) near5 (type\$1 format\$1 standard\$1) near5 (copy\$3 transfer\$4)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 17:15
L17	24	((memory smart ic chip circuit) adj1 card\$1) near5 (adapter reader terminal interface) near5 (plural\$3 many several multiple first second) near5 (copy\$3 transfer\$4)	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 17:16